Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 10 A



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PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10 A			
V _R	150 V			
V _F at I _F	0.66 V			
I _{RM} max.	5 mA at 125 °C			
T _J max.	175 °C			
E _{AS}	2.45 mJ			
Package	TO-220AB 3L			
Circuit configuration	Common cathode			

FEATURES

- 175 °C T_J operation
- · Low forward voltage drop
- High frequency operation



FREE

- HALOGEN • High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Rectangular waveform	20	А				
V _{RRM}		150	V				
I _{FSM}	t _p = 5 μs sine	1030	А				
V _F	10 A _{pk} , T _J = 125 °C (per leg)	0.66	V				
TJ	Range	-55 to +175	°C				

VOLTAGE RATINGS						
PARAMETER SYMBOL VS-20CTQ150-M3			UNITS			
Maximum DC reverse voltage	V _R 150		V			
Maximum working peak reverse voltage	V _{RWM}	150	v			

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	PARAMETER SYMBOL TEST CONDITIONS			VALUES	UNITS			
Maximum average forward per leg current, see fig. 5 per device			50 % duty cycle at T_{C} = 154 °C, rectangular waveform		10	A		
		I _{F(AV)}			20			
Maximum peak one cycle no	Maximum peak one cycle non-repetitive		5 μs sine or 3 μs rect. pulse	Following any rated load	1030	A		
surge current per leg, see fig. 7		IFSM	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	180			
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 0.7 A, L = 10 mH		2.45	mJ		
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		0.7	А		

Revision: 22-Dec-2021

Document Number: 96246

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ELECTRICAL SPECIFICATIONS

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PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS		
		10 A	T.I = 25 °C	0.80	0.88	V		
Maximum forward voltage drop per leg See fig. 1	V (1)	20 A	1j=25 C	0.90	1.0			
	V _{FM} ⁽¹⁾	10 A	T 105 %O	0.63	0.66			
		20 A	T _J = 125 °C	0.73	0.77			
Maximum reverse leakage current per leg	I _{RM}	T _J = 25 °C	$V_{\rm B}$ = Rated $V_{\rm B}$	3.0	25	μA		
See fig. 2		T _J = 125 °C	$v_{\rm R} = haleu v_{\rm R}$	2.7	5.0	mA		
Typical junction capacitance per leg	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		-	280	pF		
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		-	8.0	nH		
Maximum voltage rate of change	dV/dt	Rated V _R		-	10 000	V/µs		

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		- 55 to 175	°C			
Maximum thermal resistance, junction to case per leg			2.0				
Maximum thermal resistance, junction to case per package	– R _{thJC}	DC operation	1.0	°C/W			
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, smooth, and greased	0.50				
Approvimeto weight			2	g			
Approximate weight			0.07	oz.			
Mounting torque	1		6 (5)	kgf ⋅ cm			
Mounting torque maximum			12 (10)	(lbf ⋅ in)			
Marking device		Case style 3L TO-220AB	20CT	Q150			



VS-20CTQ150-M3

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Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)



Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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At any rated load condition and with rated V_{RRM} applied

t_n - Square Wave Pulse Duration (µs) Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

1000

10 000

following surge 1.1.111

100

100 10

Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

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ORDERING INFORMATION TABLE

Device code	VS-	20	с	т	Q	150	-M3
		2	3	4	5	6	7
	2 3 4 5	- Cur - Circ - C = - Pac T = - Sch	rrent rati cuit conf Commo kage: TO-220 nottky "C	ng (20 = iguration on catho))" series	n: ode		
		- Env	vironmer	ntal digit	0 = 150 ; , RoHS-		ant, and

ORDERING INFORMATION (Example)						
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION						
VS-20CTQ150-M3	TQ150-M3 50 Antistatic plastic tubes					

LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?96154					
Part marking information	www.vishay.com/doc?95028				



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TO-220AB 3L

DIMENSIONS in millimeters and inches



⊕0.015@BA@



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STINDUL	MIN.		MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

MILLIMETERS	INCHES

Conforms to JEDEC[®] outline TO-220AB

SYMBOL			INCILO		NOTES
	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
E	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Notes

 $^{(1)}\,$ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension and finish uncontrolled in L1

(3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁴⁾ Dimension b1, b3, and c1 apply to base metal only

(5) Controlling dimensions: inches

⁽⁶⁾ Thermal pad contour optional within dimensions E, H1, D2, and E1

⁽⁷⁾ Outline conforms to JEDEC[®] TO-220, except D2

Revision: 22-Feb-2024

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Revision: 01-Jan-2025

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